

PATENT
005702-20035 (81788.0020)#22E
R
2/26/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Shota KITAMURA et al.

Serial No: 09/392,865

Filed: September 9, 1999

For: NON-VOLATILE SEMICONDUCTOR
MEMORY DEVICE AND ITS
MANUFACTURING METHOD

Art Unit: 2811

Examiner: Thien F. Tran

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AMENDMENT UNDER 37 C.F.R. § 1.116

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Final Office Action dated December 28, 2001, please
amend the above-identified application as follows:

IN THE CLAIMS:

Rewrite claim 16 as follows:

16. (Twice Amended) A nonvolatile semiconductor memory device
comprising:
a semiconductor substrate;
memory transistors formed on said semiconductor substrate to perform
nonvolatile storage of an electric charge in accordance with data, each of said

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HOGAN & HARTSON L.L.P.

500 SOUTH GRAND AVENUE
SUITE 1900
LOS ANGELES, CA 90071

Tel: (213) 337-6700
Fax: (213) 337-6701

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IMPORTANT NOTICE
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TO: Examiner Thien F. Tran
U.S. Patent and Trademark Office
Group Art Unit 2811

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FROM: John P. Scherlacher
Voice: (213) 337-6846
Fax: (213) 337-6701
jpscherlacher@hhlaw.com

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MESSAGE:

RE: U.S. Patent Application Serial No. 09/392,865 filed September 9, 1999
Applicant: Shota KITAMURA; For: NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE AND ITS MANUFACTURING METHOD

-- John P. Scherlacher, Reg. No. 23,009
Direct Phone No. (213) 337-6846

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S.N. 09/392,865 File No. 005702-20035/81788.0020 Date Mailed 02/25/02 By: JPS/rm
Title: NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE AND ...

(Client Name) Toshiba Corp.

The Following, due 03/28/02 in the U.S. Patent & Trademark Office
was received in the Patent & Trademark Office on the date stamped hereon:

- ☒ Amendment
☒ Letter of Transmittal
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